9600 Baud, 8 Data bits, 2 Stop, NP
(With USB Adaptor, no hardware control)
Note same pinout as S100Computers Serial I/O Board
DATA TO PDP-11
DATA FROM PDP-11
Final Board
28C64 EEPROMS
(C000H - 140000 Octal)
6264_SRAM_(8KX8) or 2764 or 28C64
Low=Read
High=Write
High-> Low to activate CPU
Power Up Circuit
Switch S100 bus lines only
IO Ports 3FE000- 3FFFFF change to 3F0000-3F0FFF or 3F0000-3F1FFF
High to stretch a bus cycle (wait states)
UART Address 3FFF70-3FFF77
WORD BIDIRECTIONAL (HighByte)
BYTE WRITE for Odd Address
BYTE READ for Even Address
WORD BIDIRECTIONAL (Low Byte)
BYTE WRITE for Even Address
PDP11 ACTIVE
Port 0EDH
Boot Address
LOW= No FPU
HIGH -Power OK
HALT Option
DAL 2 1
0 0 = Trap to location 24 (14H)
0 1 = Enter console ODT
1 0 = Go to 177773000 (3FF600)
1 1 = User defined (Bits 15-9)
(C000 = 1100000)
Atmel JTAG-A Socket
Rockwell JTAG Socket
Oscillator 20 MHz
To/From S100 bus
Card Ejector Mounting Holes
Ready goes HIGH if wait states are required
Pololu 5Volt 2.5 Amp Regulator (Optional)
S100_MALE
S100Computers.com
File: PDP J-11.sch
Sheet: /
Title: Raspberry Pi Module Board.sch
Size: D
Date: 6 apr 2017
Rev: V1.0
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